



PATENT  
ACT-320COA

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2819

Examiner: Le, Don P.

Serial No.: 10/024,661

Filed: December 13, 2001

In re Application of: Khaled Ahmad El-Ayat

For: PROGRAMMABLE MULTI-STANDARD I/O ARCHITECTURE FOR FPGAs

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail, in an envelope addressed to Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 12/18/03.

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**PRELIMINARY AMENDMENT**

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**INTRODUCTORY COMMENTS**

This responds to the office action mailed on August 18, 2003. Claims 1-15 are pending in the present application. Claims 9-15 have been withdrawn from consideration. Claims 1-3 and 6-8 have been rejected. Claims 4 and 5 were objected to. Reconsideration and allowance of the claims is respectfully requested in view of the following remarks.